

Working definition of the per-event data structure per CAMAC crate in T979
format for 2-MCP plus one crossed scintillator per CAMAC crate

CRATE #1 (Closest to Beam)

Word	Device	Name	Description	Notes
1	3377	TDC header	3377 fixed header	header word that describes 3377 soft device ID plus TDC state
2	3377	TDC 1	scintillator 1	TOF of 1st scintillator (ns resolution) relative to common stop
3	3377	TDC 2	scintillator 2	TOF of 2nd scintillator (ns resolution) relative to common stop
4	3377	TDC 3	Cherenkov ID	timing data from upstream counter for particle ID
5	4300B	FERA 1	pulse height 1	integral pulse height taken from preamp monitor output
6	4300B	FERA 2	pulse height 2	integral pulse height taken from preamp monitor output
7	4300B	FERA 3	Real-time clock	Measurement of a slow ramp via a 4300B FERA ADC
8	AD114 #1	ADC 1	MCP #1	TOF (MCP hit to common stop) measured using CFD/TAC/ADC
9	AD114 #2	ADC 2	MCP #2	TOF (MCP hit to common stop) measured using CFD/TAC/ADC
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CRATE #2 (Farther Downstream)

Word	Device	Name	Description	Notes
1	3377	TDC header	3377 fixed header	header word that describes 3377 soft device ID plus TDC state
2	3377	TDC 1	scintillator 1	TOF of 1st scintillator (ns resolution) relative to common stop
3	3377	TDC 2	scintillator 2	TOF of 2nd scintillator (ns resolution) relative to common stop
4	3377	TDC 3	spare/unused	Crate #1 has three TDC channels used, should use this to keep data consistent
5	4300B	FERA 1	pulse height 1	integral pulse height taken from preamp monitor output
6	4300B	FERA 2	pulse height 2	integral pulse height taken from preamp monitor output
7	4300B	FERA 3	Real-time clock	Measurement of a slow ramp via a 4300B FERA ADC
8	AD114 #1	ADC 1	MCP #1	TOF (MCP hit to common stop) measured using CFD/TAC/ADC
9	AD114 #2	ADC 2	MCP #2	TOF (MCP hit to common stop) measured using CFD/TAC/ADC
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