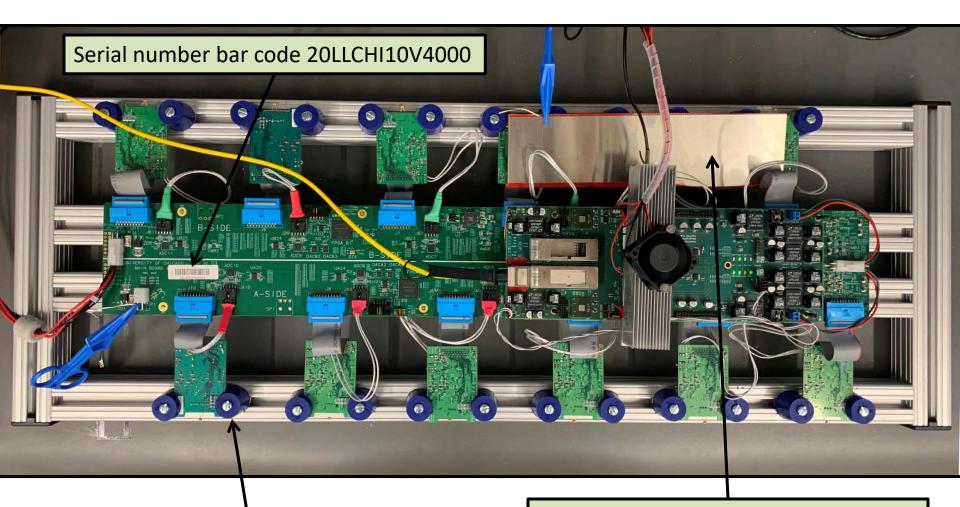
Test Stand It looks a lot like the Clermont Ferrand test stand since I copied their setup.

We plan to have 4 test stands with DBv6s



Band Saw sliced 1 hole bottle stoppers.

Shield from the thermo-electric cooler and fan power leads ... the integrator ADCs are very sensitive to noise. After Burn-In is completed, we need to do final test of all of the functions of the mainboards before sending them to CERN or other labs.

Step 1 For burn-in, special firmware is used for the FPGAs that uses the onboard 40MHz clock.

Step 2

 -bash-4.2\$ python ResetLinksv5.py -m 1 -c AO -d RX -b DB -g A communicating with host communication successful Resetting wait 1 second...

Resetting FPGA A wait 1 second...

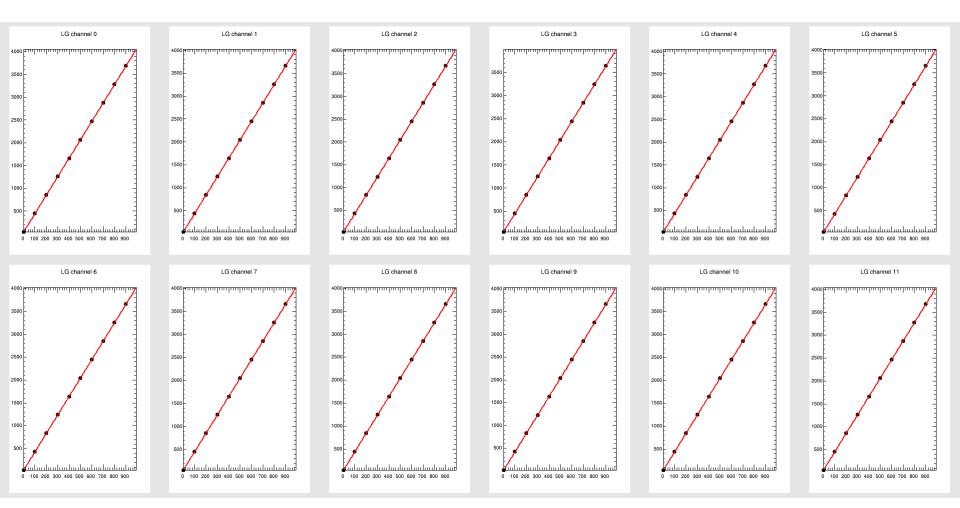
	TX PLL	RX_FRAME	====== ECLK -	· RX WORDCLI	< - MG	====== T RDY	- BITS	.IPNb	- GE	STRX RDY	- GBTT)	(RDY	LOST	======= - DATA	ERROR	-
SideA0:	- 0	- 1 - 1 - 1 - 1 - 1	-	· 1	-	1	-	0	-	1	-	0	-	0		
SideA1:	- õ	- 1	-	· 1	-	1	-	õ	-	ī	-	õ	-	ŏ		
SideB0+	- Ň	- 1	_	· 1	_	1	_	ň	_	1	_	ň	_	ŏ		
SideP1+	- ŏ	- 1	_	. 1	_	1	_	ň	_	1	_	ň	_	ŏ		
				·						<u>+</u>						
		- RX_FRAME														
2/1-00.	IN_FLL	KA_EKHNC	IULN -	· KA_WUKUULI	< - ma		- 6110		- GE		- 6011/	/_KD1_	LUSI	- DHIH	ERRUR	-
51deHU:	- 0	- 1	-	· 1	-	1	-	ů.	-	1	-	ů,	-	, v		
51deH1:	- 0	- 1	-	· 1	-	1	-	0	-	1	-	0	-	0		
SideB0:	- 0	- 1	-	· 1	-	1	-	0	-	1	-	0	-	0		
SideB1:	- 0	- 1 - 1 - 1 - 1	-	· 1	-	1	-	0	-	1	-	0	-	0		
							======						=====			
	TX_PLL	. – RX_FRAME	ECLK -	 RX_WORDCLH 	< - MG	T_RDY	- BITS	.IPNb	- GE	STRX_RDY	- GBTT>	<_RDY_			ERROR	-
SideA0:	- 0	- 1	-	· 1	-	1	-	0	-	1	-	0	-	0		
SideA1:	- 0	- 1	-	· 1	-	1	-	0	-	1	-	0	-	0		
SideB0:	- 0	- 1	-	· 1	-	1	-	Ó	-	1	-	Ō	-	Ó	T	ř
SideB1 t	- õ		-	· 1	-	1	_	õ	-	1	-	õ	-	ŏ	Ţ	
=======																
	TV DUL	DV EDOM		. PX MORTON	< - MC	T DDV	DITC	TON	CT.	TOV DDV	- CRIT		LOST	- ПОТО	FRROR	_
Side00+		4		. 4	_ na	1	-	0	GE	1	-	0	.2031	0,00	ERROR	
SideHUI	- 0	- 1		1	-	1		ő	-	1	-	Ň		Ň		
bideH1:	- 0	- 1	-	- 1	-	1	-	0	-	1	-	0	-	0		
ideB0:	- 0	- 1	-	- 1	-	1	-	0	-	1	-	0	-	0		
SideB1:	- 0	KX_FRHM - 1 - 1 - 1 - 1	-	• 1	-	1	-	0	-	1	-	0	-	0		
· `	TX_PLL	. – RX_FRAME	ECLK -	 RX_WORDCLH 	< - MG	T_RDY	- BITS	.IPNb	- GE	STRX_RDY	- GBTT>	(_RDY_	LOST	- DATA_	ERROR	-
SideA0:	- 0	1 1 - 1 - 1	-	· 1	-	1	-	0	-	1	-	0	-	0		
SideA1:	- 0	- 1	-	· 1	-	1	-	Ô.	-	1	-	Ó	-	Ó		
SideBOt	- 0	- 1	_	· 1	-	1	-	Ô.	_	1	_	Ô.	-	ŏ		
SideB1+	- ŏ	- 1	_	· 1	_	1	_	ň	_	1	_	ň	_	ň		
))	· · ·	1		1		-		× .		-		Ŷ		× .		
,																
				· Bit Eri	ror Ra	te mea	suremen	nts, Mi	.nidr	rawer 1						
1	Number	of frames	- URU	: Frame Erro	ors - I	Fracti	on per	millio	n -	Bit Erro	r Kate	- E++	ectiv	e Error	s	
SideAO		160541227		0						0.000000	i⇔+00			0		
SideA1							~			· • • • • • • • • • • • • • • • • • • •	6.00					
SideB0		160541227		0			ŏ			0.000000	e+00			ŏ		
		160541227 160541227		0			ŏ			0.000000	e+00 e+00					
SideB1		160541227 160541227 160541227		0 0 0 0			Ö O O			0.000000	le+00 le+00 le+00			Ó		
SideB1		160541227 160541227 160541227 160541227		0 0 0			0 0 0			0.000000 0.000000 0.000000	le+00 le+00 le+00			Ó		
SideB1		160541227 160541227 160541227		0 0 0			0 0			0.000000 0.000000 0.000000	le+00 le+00 le+00			Ó		
5ideB1) 		160541227		0			0			0.000000	e+00			0 0 0		
5ideB1) 		160541227 160541227 160541227		0 Bit Eri	 ror Ra	 te mea	0 suremen	nts. Mi		0.000000 	e+00			0 0 0		
5ideB1) 		160541227		0 Bit Eri	ror Ra	 te mea	0 suremer	nts. Mi	nidr	0.000000 nawer 1	e+00			0 0 		
SideB1) 	 Number	160541227		0 Bit Ern Frame Erro	ror Ra	 te mea Fracti	0 surements on per	nts. Mi millic	nidr	0.000000 rawer 1 Bit Erro	e+00 r Rate			0 0 0		
SideB1) SideA0	Number	160541227		0 Bit Ern Frame Erro	ror Ra	 te mea Fracti	0 surements on per	nts. Mi millic	nidr	0.000000 rawer 1 Bit Erro	e+00 r Rate			0 0 0 e Error 0		
SideB1) SideA0	Number	160541227		0 Bit Ern Frame Erro	ror Ra	 te mea Fracti	0 suremen on per 0 0	nts. Mi millic	.nidr n -	0.000000 rawer 1 Bit Erro 0.000000 0.000000	e+00 nr Rate le+00 le+00			0 0 e Error 0 0		
SideB1) SideA0	Number	160541227		0 Bit Ern Frame Erro	ror Ra	 te mea Fracti	0 suremen on per 0 0 0	nts. Mi millic	nidr n -	0.000000 rawer 1 Bit Erro 0.000000 0.000000 0.000000	e+00 			0 0 0 e Error 0		
SideB1	Number	160541227		0 Bit Ern Frame Erro	ror Ra	 te mea Fracti	0 suremen on per 0 0	nts. Mi millic	nidr n -	0.000000 rawer 1 Bit Erro 0.000000 0.000000	e+00 			0 0 e Error 0 0		
SideB1	Number	160541227		0 Bit Ern Frame Erro	ror Ra	 te mea Fracti	0 suremen on per 0 0 0	nts. Mi millic	nidr n -	0.000000 rawer 1 Bit Erro 0.000000 0.000000 0.000000	e+00 			0 0 e Error 0 0		
SideB1	Number	160541227	- CRC	0 Bit Err Frame Erro 0 0 0 0 0	ror Ra ors - I	te mea Fracti	0 suremen on per 0 0 0	nts. Mi millic	nidr m -	0.000000 rawer 1 Bit Erro 0.000000 0.000000 0.000000	He+00 He +00 He+00 He+00 He+00 He+00	- Eff	èctiv	0 0 0 e Error 0 0 0 0		
SideB1	Number	160541227 of frames 160534329 160534329 160534329 160534329	- CRC	0 Bit Err Frame Erro 0 0 0 0 0 0 0	ror Ra ors - I	te mea Fracti te mea	0 suremen on per 0 0 0 0	nts. Mi millic	nidr n -	0.000000 rawer 1 Bit Erro 0.000000 0.000000 0.000000 0.000000	le+00 le +00 le+00 le+00 le+00 le+00	- Eff	èctiv	0 0 0 e Error 0 0 0 0		
SideB1	Number	160541227	- CRC	0 Bit Err Frame Erro 0 0 0 0 0 0 0	ror Ra ors - I	te mea Fracti te mea	0 suremen on per 0 0 0 0	nts. Mi millic	nidr n -	0.000000 rawer 1 Bit Erro 0.000000 0.000000 0.000000 0.000000	le+00 le +00 le+00 le+00 le+00 le+00	- Eff	èctiv	0 0 0 e Error 0 0 0 0		
SideB1	Number	160541227 of frames 160534329 160534329 160534329 160534329	- CRC	0 Bit Ern Frame Erro 0 0 0 0 0 0	ror Ra ors - I ror Ra	te mea Fracti te mea	0 suremen 0 0 0 0 0 0 0 0	nts. Mi millic nts. Mi	.nidr on -	0.000000 Tawer 1 Bit Erro 0.000000 0.000000 0.000000 0.000000	le+00 m Rate le+00 le+00 le+00 le+00 le+00 le+00	- Eff	ectiv	0 0 0 e Error 0 0 0 0	`S	
SideB1	Number	160541227 of frames 160534329 160534329 160534329 160534329 	- CRC	• Bit Err • Frame Erro 0 0 0 • Bit Erro • Bit Erro	ror Ra	te mea Fracti Fracti te mea Fracti	0 suremen on per 0 0 0 0 0 suremen on per	nts. Mi millic nts. Mi millic	.nidr m -	0.000000 Tawer 1 Bit Erro 0.000000 0.000000 0.000000 0.000000 Tawer 1 Bit Erro	e+00 r Rate e+00 e+00 e+00 e+00 r Rate	- Eff	ectiv	0 0 0 e Error 0 0 0 0 0	`S	
SideB1	Number Number	160541227 of frames 160534329 160534329 160534329 160534329 	- CRC	0 Bit Ern Frame Ern 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	ror Ra ors - I ror Ra	te mea Fracti Fracti te mea Fracti	0 suremen on per 0 0 0 0 0 suremen on per 0	nts. Mi millic nts. Mi millic	.nidr on - .nidr	0.000000 Tawer 1 Bit Erro 0.000000 0.000000 0.000000 Tawer 1 Bit Erro 0.000000	e+00 r Rate e+00 e+00 e+00 e+00 r Rate e+00	- Eff	ectiv	0 0 0 e Error 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	`S	
SideB1	Number Number	160541227 of frames 160534329 160534329 160534329 160534329 160534329 of frames 160530424	- CRC	0 Bit Err Frame Erro 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	ror Ra ors - I ror Ra	te mea Fracti Fracti	0 suremen on per 0 0 0 0 0 0 suremen on per 0 0	nts. Mi millic nts. Mi millic	.nidr m - .nidr	0.000000 awer 1 Bit Erro 0.000000 0.000000 0.000000 awer 1 Bit Erro 0.000000 0.000000	e+00 r Rate e+00 e+00 e+00 e+00 r Rate e+00 r Rate e+00 e+00	- Eff	ectiv	0 0 0 e Error 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	`S	
SideB1	Number	160541227 of frames 160534329 160534329 160534329 160534329 	- CRC	0 Bit Ern Frame Ern 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	ror Ra ors - I ror Ra	te mea Fracti Fracti	0 suremen 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	nts. Mi millic nts. Mi millic	.nidr m - .nidr	0.000000 awer 1 Bit Erro 0.00000 0.000000 0.000000 0.000000 awer 1 Bit Erro 0.000000 0.000000	e+00 r Rate e+00 e+00 e+00 e+00 r Rate e+00 e+00 e+00	- Eff	ectiv	0 0 0 e Error 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	`S	
GideB1	Number	160541227 of frames 160534329 160534329 160534329 160534329 	- CRC	0 Bit Err 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	ror Ra ors - I ror Ra	te mea Fracti Fracti	0 suremen on per 0 0 0 0 0 0 suremen on per 0 0	nts. Mi millic nts. Mi millic	.nidr m - .nidr	0.000000 awer 1 Bit Erro 0.000000 0.000000 0.000000 awer 1 Bit Erro 0.000000 0.000000	e+00 r Rate e+00 e+00 e+00 e+00 r Rate e+00 e+00 e+00	- Eff	ectiv	0 0 0 e Error 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	`S	
SideB1	Number	160541227 of frames 160534329 160534329 160534329 160534329 	- CRC	0 Bit Err 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	ror Ra ors - I ror Ra	te mea Fracti Fracti	0 suremen 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	nts. Mi millic nts. Mi millic	.nidr m - .nidr	0.000000 awer 1 Bit Erro 0.00000 0.000000 0.000000 0.000000 awer 1 Bit Erro 0.000000 0.000000	e+00 r Rate e+00 e+00 e+00 e+00 r Rate e+00 e+00 e+00	- Eff	ectiv	0 0 0 e Error 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	`S	
SideB1	Number 	160541227 of frames 160534329 160534329 160534329 160534329 	- CRC	0 Bit Ern Frame Erro 0 0 0 Bit Ern Frame Erro 0 0 0 0 0 0	ror Ra ors - I ror Ra ors - I	te mea Fracti te mea Fracti	0 suremen 0 0 0 0 0 suremen 0 0 0 0 0 0	nts. Mi millic nts. Mi millic	nidr n – nidr	0.000000 awer 1 Bit Erro 0.00000 0.000000 0.000000 0.000000 awer 1 Bit Erro 0.000000 0.000000 0.000000 0.000000	e+00 rr Rate e+00 e+00 e+00 e+00 rr Rate e+00 e+00 e+00 e+00 e+00	- Eff 	`ectiv `ectiv	0 0 0 e Error 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	`S	
SideB1	Number 	160541227 of frames 160534329 160534329 160534329 160534329 	- CRC	0 Bit Ern Frame Erro 0 0 0 Bit Ern Frame Erro 0 0 0 0 0 0	ror Ra ors - I ror Ra ors - I	te mea Fracti te mea Fracti	0 suremen 0 0 0 0 0 suremen 0 0 0 0 0 0	nts. Mi millic nts. Mi millic	nidr n – nidr	0.000000 awer 1 Bit Erro 0.00000 0.000000 0.000000 0.000000 awer 1 Bit Erro 0.000000 0.000000 0.000000 0.000000	e+00 rr Rate e+00 e+00 e+00 e+00 rr Rate e+00 e+00 e+00 e+00 e+00	- Eff 	`ectiv `ectiv	0 0 0 e Error 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	`S	
SideB1	Number 	160541227 of frames 160534329 160534329 160534329 160534329 	- CRC	0 Bit Ern Frame Erro 0 0 0 Bit Ern Frame Erro 0 0 0 0 0 0	ror Ra ors - I ror Ra ors - I	te mea Fracti te mea Fracti	0 suremen 0 0 0 0 0 suremen 0 0 0 0 0 0	nts. Mi millic nts. Mi millic	nidr n – nidr	0.000000 awer 1 Bit Erro 0.00000 0.000000 0.000000 0.000000 awer 1 Bit Erro 0.000000 0.000000 0.000000 0.000000	e+00 rr Rate e+00 e+00 e+00 e+00 rr Rate e+00 e+00 e+00 e+00 e+00	- Eff 	`ectiv `ectiv	0 0 0 e Error 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	`S	
SideB1	Number Number	160541227 of frames 160534329 160534329 160534329 160534329 160534329 of frames of frames 160530424 160530424 160530424 160530424	- CRC	0 Bit Ern Frame Erro 0 0 0 Bit Ern 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	ror Ra ors - I ors - I ors - I	te mea	0 suremen 0 0 0 0 0 suremen 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	nts. Mi millic nts. Mi millic	nidr n - nidr	0.000000 Tawer 1 Bit Erro 0.00000 0.0000000 0.000000 0.000000 0.000000 0.000000 0.000000 0.000000 0.000000 0.000000 0.000000 0.000000 0.000000 0.000000 0.000000 0.000000 0.000000 0.000000 0.000000 0.000000 0.000000 0.000000 0.000000 0.000000 0.000000 0.000000 0.000000 0.0000000 0.00000000	e+00 rr Rate e+00 e+00 e+00 e+00 rr Rate e+00 e+00 e+00 e+00	- Eff 	`ectiv `ectiv	0 0 0 e Error 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	2	
SideB1	Number Number Number	160541227 of frames 160534329 160534329 160534329 160534329 160534329 of frames 160530424 160530424 160530424 160530424 160530424	- CRC	Bit Ern Frame Erro 0 0 0 Bit Ern Frame Erro 0 0 0 0 0 0 0 0 0	ror Ra ors - I ors - I ors - I	te mea	0 suremen 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	nts. Mi millic nts. Mi millic nts. Mi	nidr m - nidr m -	0.000000 awer 1 Bit Erro 0.00000 0.000000 0.000000 awer 1 Bit Erro 0.000000 0.000000 0.000000 0.000000 0.000000 0.000000 0.000000 0.000000 0.000000 0.000000 0.000000 awer 1 Bit Erro	e+00 rr Rate e+00 e+00 e+00 e+00 rr Rate e+00 e+00 e+00 e+00 rr Rate	- Eff 	`ectiv `ectiv	0 0 0 e Error 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	2	
SideB1	Number Number Number	160541227 of frames 160534329 160534329 160534329 160534329 160530424 160530424 160530424 160530424 160530424 160530424	- CRC	Bit Ern Frame Erro 0 0 0 Bit Ern Frame Erro 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	ror Ra ors - I ors - I ors - I	te mea	0 suremen 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	nts. Mi millic nts. Mi millic	nidr n - nidr n -	0.000000 awer 1 Bit Erro 0.00000 0.000000 0.000000 awer 1 Bit Erro 0.000000 0.000000 0.000000 0.000000 awer 1 Bit Erro 0.000000	e+00 m Rate e+00 e+00 e+00 e+00 m Rate e+00 e+00 e+00 m Rate e+00 m Rate e+00	- Eff 	`ectiv `ectiv	0 0 0 e Error 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	2	
SideB1 SideA0 SideB1 SideB1 SideB1 SideB1 SideA1 SideB1 SideB1 SideB1 SideB1 SideB1 SideB1 SideB1 SideA1 SideA1 SideA1 SideA0 SideA1 SideA1 SideA0 SideA1 SideA1 SideA0 SideA1 SideA1 SideA0 SideA1 SideA0 SideB1 SideA0 SideB1 SideA0 SideB1 SideA0 SideB1 SideA0 SideB1 SideA0 SideB1 SideA0 SideB1 SideB1 SideB1 SideB1 SideB1 SideB1 SideB1 SideB1 SideB1 SideB1 SideB1 SideB1 SideB1 SideB1 SideB1 SideB1 SideB1 SideB0 SideB1 SideB0 SideB1 SideB0 SideB1 SideB0 SideB1 SideB0 SideB1 SideB0 SideB1 SideB0 SideB1	Number Number Number	160541227 of frames 160534329 160534329 160534329 160534329 	- CRC	Bit Err Frame Erro 0 0 0 0 Bit Err Frame Erro 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	ror Ra ors - I ors - I ors - I	te mea	0 suremen 0 0 0 0 0 suremen 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	nts. Mi millic nts. Mi millic nts. Mi	nidr n - nidr n -	0.000000 awer 1 Bit Erro 0.000000 0.000000 0.000000 0.000000 0.000000	e+00 r Rate e+00 e+00 e+00 r Rate e+00 e+00 e+00 r Rate e+00 r Rate e+00 r Rate e+00	- Eff 	`ectiv `ectiv	0 0 0 e Error 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	2	
SideB1	Number Number Number	160541227 of frames 160534329 160534329 160534329 160534329 160534329 160530424 160530424 160530424 160530424 160530424 160530424 160530424 160528644 160528644	- CRC	Bit Ern Frame Erro 0 0 0 Bit Ern Frame Erro 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	ror Ra ors - I ors - I ors - I	te mea	0 suremen 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	nts. Mi millic nts. Mi millic nts. Mi	nidr n - nidr n -	0.000000 awer 1 Bit Erro 0.00000 0.000000 0.000000 awer 1 Bit Erro 0.000000 0.000000 0.000000 0.000000 awer 1 Bit Erro 0.0000000 0.00000000	e+00 rr Rate e+00 e+00 e+00 e+00 rr Rate e+00 e+00 e+00 rr Rate e+00 rr Rate e+00 rr Rate e+00 re Rate	- Eff 	`ectiv `ectiv	0 0 0 e Error 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	2	
SideB1	Number Number Number	160541227 of frames 160534329 160534329 160534329 160534329 	- CRC	Bit Err Frame Erro 0 0 0 0 Bit Err Frame Erro 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	ror Ra ors - I ors - I ors - I	te mea	0 suremen 0 0 0 0 0 suremen 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	nts. Mi millic nts. Mi millic nts. Mi	nidr n - nidr n -	0.000000 awer 1 Bit Erro 0.000000 0.000000 0.000000 0.000000 0.000000	e+00 rr Rate e+00 e+00 e+00 e+00 rr Rate e+00 e+00 e+00 rr Rate e+00 rr Rate e+00 rr Rate e+00 re Rate	- Eff 	`ectiv `ectiv	0 0 0 e Error 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	2	

Step 3 ===== serno.py ========

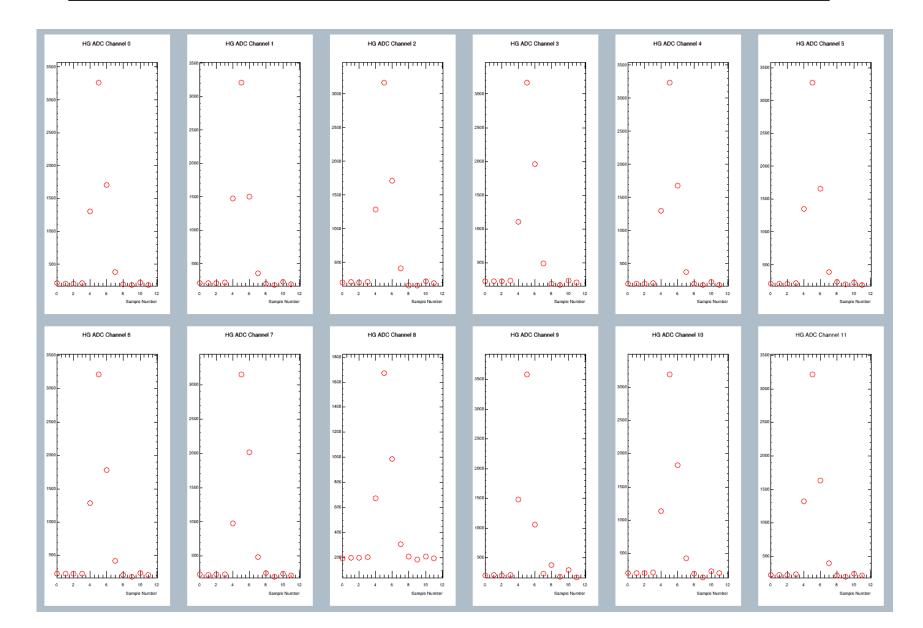
Read the board serial number and make a directory of that name to store test results.

Read/Write to all of the 3in1/FENICS card configuration registers

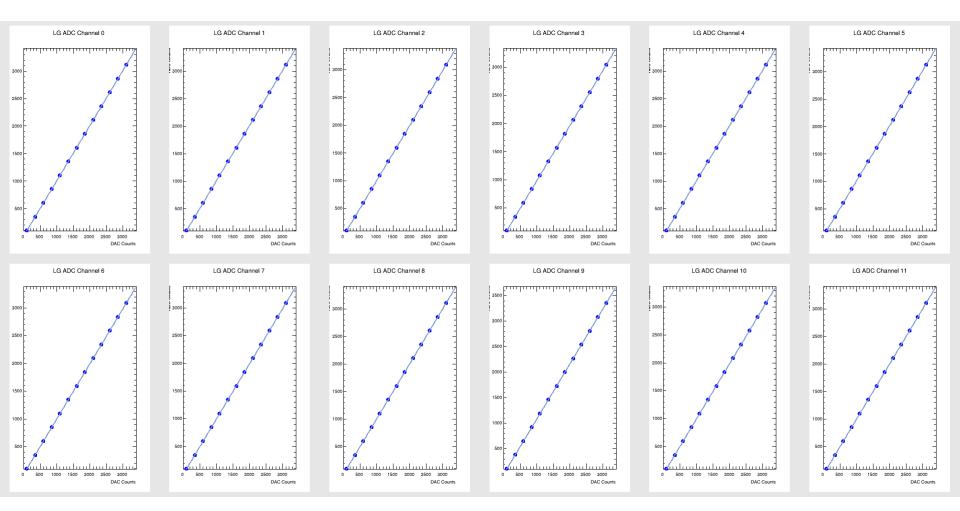
Step 4 ===== offset.py ======== plot the high and low gain offset dac vs ADC Counts



Step 5 ===== evLG.py and evHG.py ======== look at pulse shape for each ADC

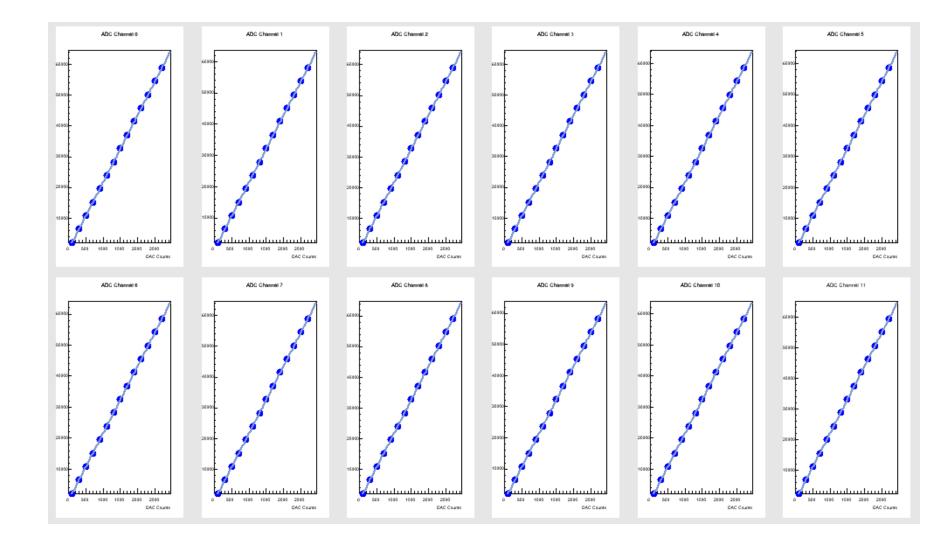


Step 6 ====== cisLG.py and cisHG.py======== plot the high and low gain charge injections dac vs ADC Counts



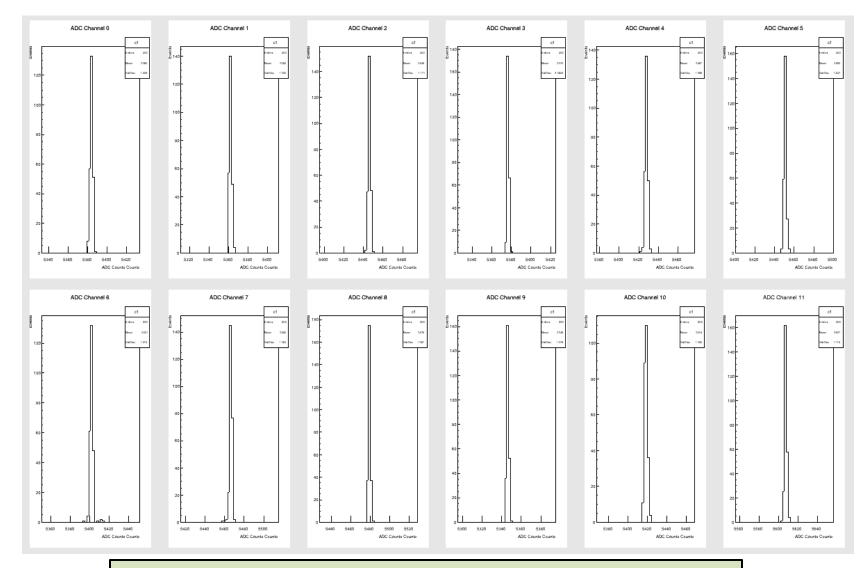
... and other plots RMS vs DAC Fit-Data vs DAC

Step 7 ===== intlin.py======= at lowest 3in1/FENICS gain setting plot DAC vs ADC Counts



Step 7 ====== intres.py========

plot the resolution at one DAC setting



If all of this looks good, we move on to the next MainBoard.

We need to process each day:

- 1. remove 4 MainBoards from a Burn-In table.
- 2. affix serial numbers on 4 new boards
- 3. program burn-In firmware on the 4 new boards
- 4. mount then in the heater boxes
- 5. scan the serial numbers and start the 5 day heat cycle
- 6. do final testing as outlined above for the 4 boards that came out of the burn-in stand.

.... shipping logistics Repairs ?

900 boards

