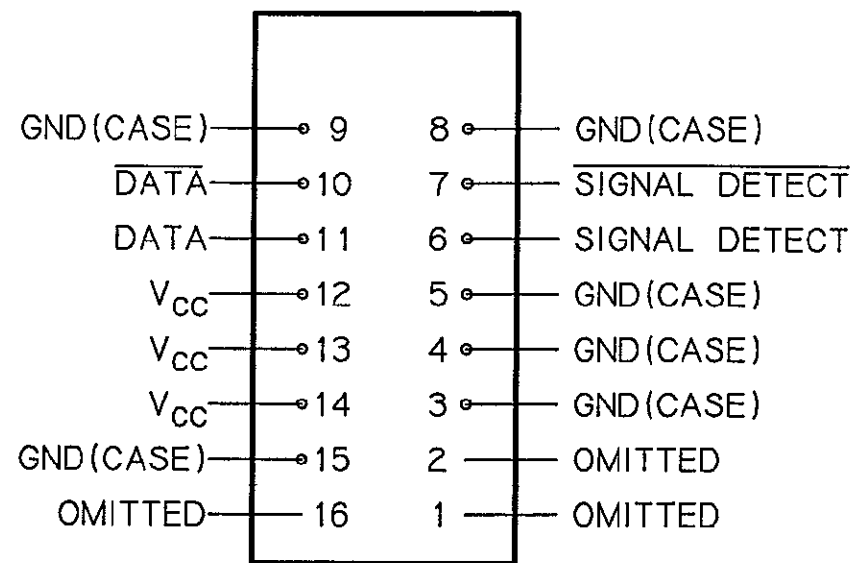
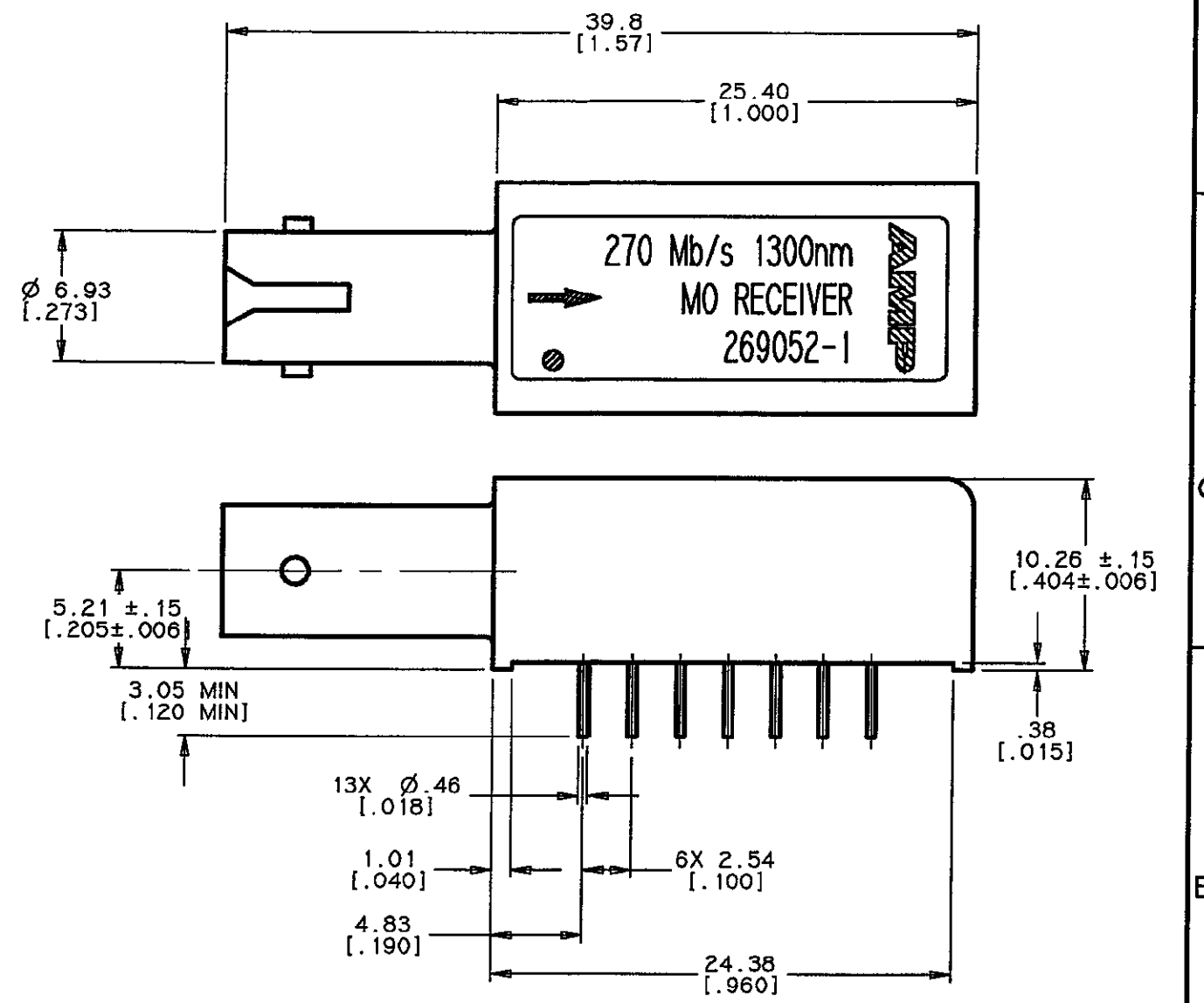
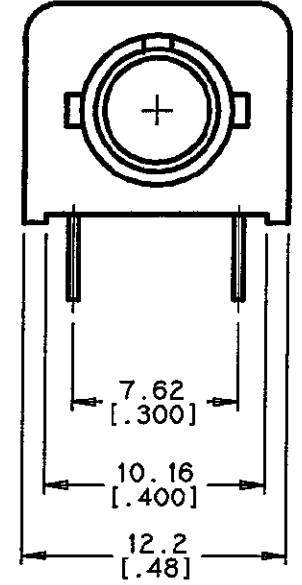


LOC	DIST	REVISIONS					
DR	01	P	LTR	DESCRIPTION	DATE	DWN	APVD
		A		EC 0D30-0052-99	22FEB99	PEM	



PINOUT WHEN VIEW FROM TOP  
 RAISED ECL V<sub>EE</sub> = GND  
 V<sub>CC</sub> = +5V ±5%



NOTES:

- DIMENSIONS IN [ ] ARE IN INCHES.
- PIN PLATING: 100 MICROINCHES MIN 93/7 SnPb OVER 50 MICROINCHES MIN NICKEL.
- MEASURED USING 2<sup>15</sup>-1 PRBS.
- Ons EYE OPENING, V<sub>CC</sub> = 5V, 25°C, 2<sup>15</sup> -1 PRBS.
- VOLTAGE LEVELS ARE COMPATIBLE WITH 100K, 10K, AND 10KH SERIES LOGIC LEVELS WHEN USED DIFFERENTIALLY.
- UNIT PROVIDED WITH PROCESS CAP TO PROTECT OPTICAL PORTS DURING SOLDERING AND CLEANING PROCESSES.
- DOES NOT INCLUDE ECL LOAD CURRENTS.
- CASE MATERIAL: NICKEL PLATED ZINC

THIS DRAWING IS A CONTROLLED DOCUMENT FOR AMP INCORPORATED. IT IS SUBJECT TO CHANGE AND THE CONTROLLING ENGINEERING ORGANIZATION SHOULD BE CONTACTED FOR THE LATEST REVISION.		DWN. P. MALARCHER	AMP Incorporated Harrisburg, PA 17105-3608	
DIMENSIONS: mm (Inches)		CHK. L. JACOB	NAME RECEIVER MODULE	
TOLERANCES UNLESS OTHERWISE SPECIFIED: 1 PLC. DEC. ±0.25 [.010] 2 PLC. DEC. ±0.13 [.005] ANGLES ±0.5 DEG.		APVD <i>Paul Malarcher 22FEB99</i>	MOLDED OPTICS, 16 PIN 2.5mm BAYONET, 270 Mb/s	
MATERIAL:		PRODUCT SPEC. 108-55003	SIZE A3	CAGE CODE -
FINISH:		APPLICATION SPEC.	DRAWING NO. ©-269052-1	
		WEIGHT:	SCALE: 3:1 SHEET 1 OF 2 REV. A	
		CUSTOMER DRAWING	PD\3485 DCA	

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3

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RELEASED FOR PUBLICATION

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LOC DR 01

## REVISIONS

P	LTR	DESCRIPTION	DATE	DWN	APVD
A		EC 0030-0052-99	-	PEM	-

PERFORMANCE SPECIFICATIONS ( $T_A = 0-70^\circ\text{C}$ ,  $V_{CC} - V_{EE} = 4.75-5.25\text{V DC}$ )

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNITS
DATA RATE (NRZ)	B	-	10	-	270	Mb/s
OPTICAL INPUT (AVG) SENSITIVITY $\triangle 4$	$P_{IN}$	0.275NA 62.5/125 $\mu\text{m}$ FIBER B=270Mb/s, BER $\leq 1.0 \times 10^{-12}$	-29.0	-	-14.0	dBm
OPTICAL WAVELENGTH	$\lambda_{IN}$	-	1270	-	1380	nm
DUTY CYCLE	-	-	40	50	60	%
OUTPUT RISE TIME	$t_{TLH}$	20-80%, 50 $\Omega$ TO $V_{CC}-2V$	.5	-	2.5	ns
OUTPUT FALL TIME	$t_{THL}$	80-20%, 50 $\Omega$ TO $V_{CC}-2V$	.5	-	2.5	ns
PULSE WIDTH DISTORTION	PWD	50 $\Omega$ TO $V_{CC}-2V$	-	-	0.4	ns
DATA DEPENDENT JITTER	$t_{DDJ}$	$\triangle 3$	-	-	0.8	ns
DATA (OUTPUT) $\triangle 5$	$V_{OH}$	50 $\Omega$ TO $V_{CC}-2V$	$V_{CC}-1.025$	-	$V_{CC}-.88$	V
	$V_{OL}$	50 $\Omega$ TO $V_{CC}-2V$	$V_{CC}-1.81$	-	$V_{CC}-1.62$	V
SIGNAL DETECT (OUTPUT) $\triangle 5$	$V_A$	$P_{IN} \geq P_A$ , 50 $\Omega$ TO $V_{CC}-2V$	$V_{CC}-1.025$	-	$V_{CC}-.88$	V
	$V_D$	$P_{IN} < P_D$ , 50 $\Omega$ TO $V_{CC}-2V$	$V_{CC}-1.81$	-	$V_{CC}-1.62$	V
SIGNAL DETECT POWER LEVELS (AVG)						
DEASSERT	$P_D$	—	-39.0	-	-31.5	dBm
ASSERT	$P_A$	—	-38	-	-30.0	dBm
HYSTERESIS			1.5	2.0	-	dB
SIGNAL DETECT DELAY TIME						
DEASSERT			-	-	100	$\mu\text{s}$
ASSERT			-	-	50	$\mu\text{s}$
POWER SUPPLY VOLTAGE	$V_{CC} - V_{EE}$	-	4.75	5.0	5.25	V
SUPPLY CURRENT $\triangle 7$	$I_{CC}$	-	-	-	110	mA
OPERATING TEMPERATURE	$T_A$	-	0	-	70	$^\circ\text{C}$
<b>ABSOLUTE MAXIMUM RATINGS</b>						
STORAGE TEMPERATURE	-	-	-40	-	100	$^\circ\text{C}$
LEAD SOLDERING LIMITS	-	-	-	-	240/10	$^\circ\text{C/s}$
POWER SUPPLY VOLTAGE	$V_{CC} - V_{EE}$	-	-0.2	-	7.00	V

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DWN. P. MALARCHER

CHK. L. JACOB

APVD *Paul Malarcher 2/28/99*

PRODUCT SPEC. 108-55003

APPLICATION SPEC.

WEIGHT.

CUSTOMER DRAWING

**AMP**AMP Incorporated  
Harrisburg, PA 17105-3608

NAME

RECEIVER MODULE  
MOLDED OPTICS, 16 PIN  
2.5mm BAYONET, 270 Mb/s

SIZE

A3

CAGE CODE

-

DRAWING NO.

©-269052-1

SCALE: 3:1

SHEET 2 OF 2

REV. A

DIMENSIONS:  
mm  
[Inches]TOLERANCES UNLESS  
OTHERWISE SPECIFIED:1 PLC. DEC.  $\pm 0.25$  [.010]  
2 PLC. DEC.  $\pm 0.13$  [.005]  
ANGLES  $\pm 0.5$  DEG.

MATERIAL:

FINISH: