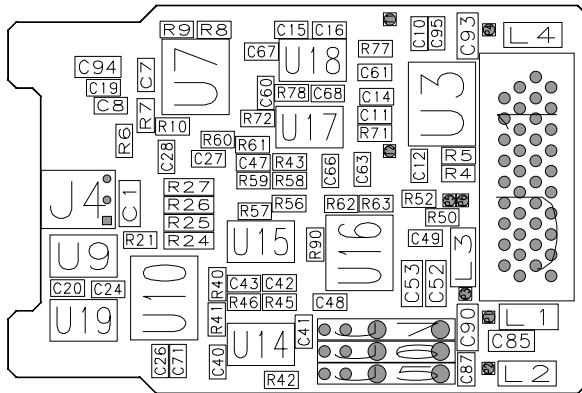
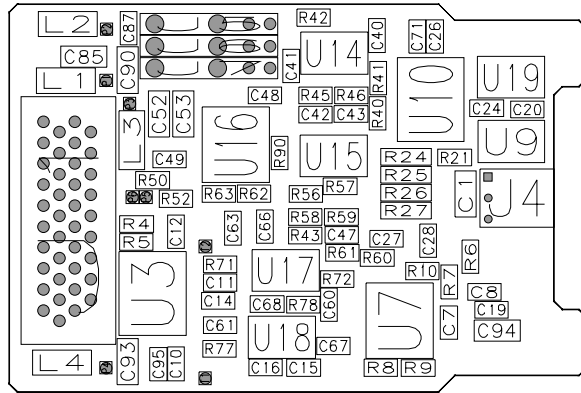


B-2382 V3.1 Top Side Assembly Drawing

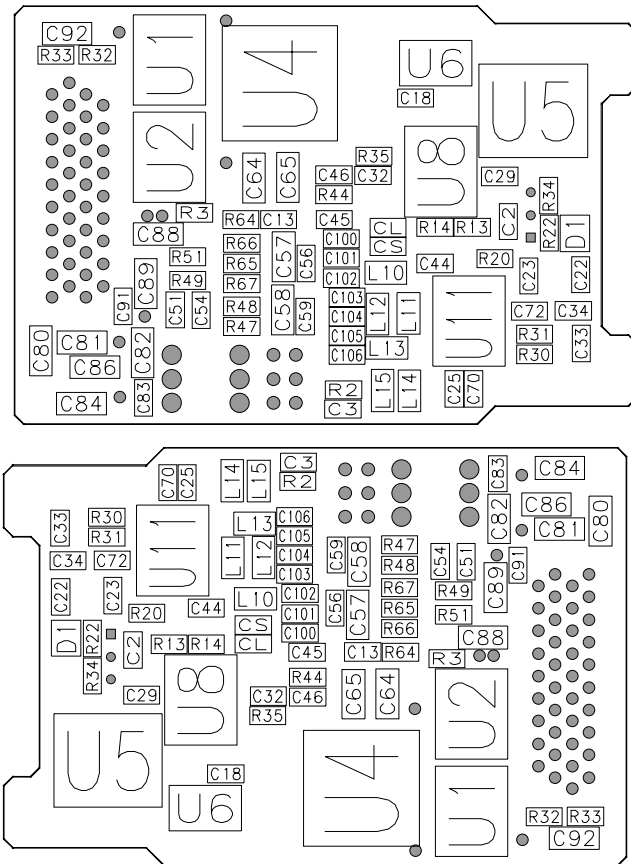


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SCHM# B-2381 V3.1
 SPEC# B-2382 V3.1
 ASSM# B-2383 V3.1

UNIVERSITY OF CHICAGO ELECTRONICS DEVELOPMENT GROUP	
TITLE B-2382 V3.1 Assembly Drawing	
SHEET 1 OF 2	B- 2382 REV v3.1
DATE 02/23/99	
DRAWN TANG	

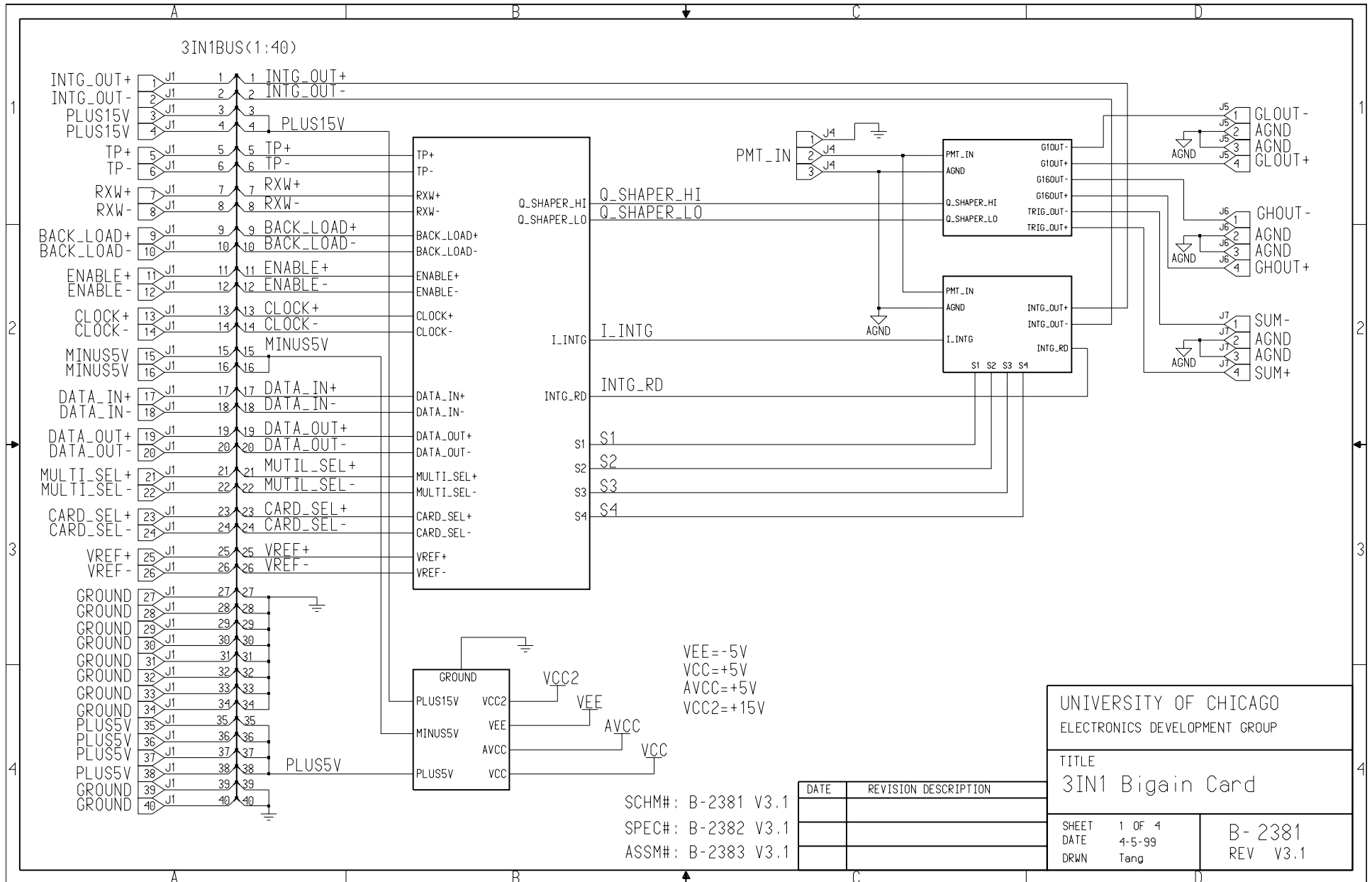
B-2382 V3.1 Bottom Side Assembly Drawing



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SCHM# B-2381 V3.1
 SPEC# B-2382 V3.1
 ASSM# B-2383 V3.1

UNIVERSITY OF CHICAGO ELECTRONICS DEVELOPMENT GROUP	
TITLE B-2382 V3.1 Assembly Drawing	
SHEET 2 OF 2	B- 2382 REV V3.1
DATE 02/23/98	
DRAWN TANG	



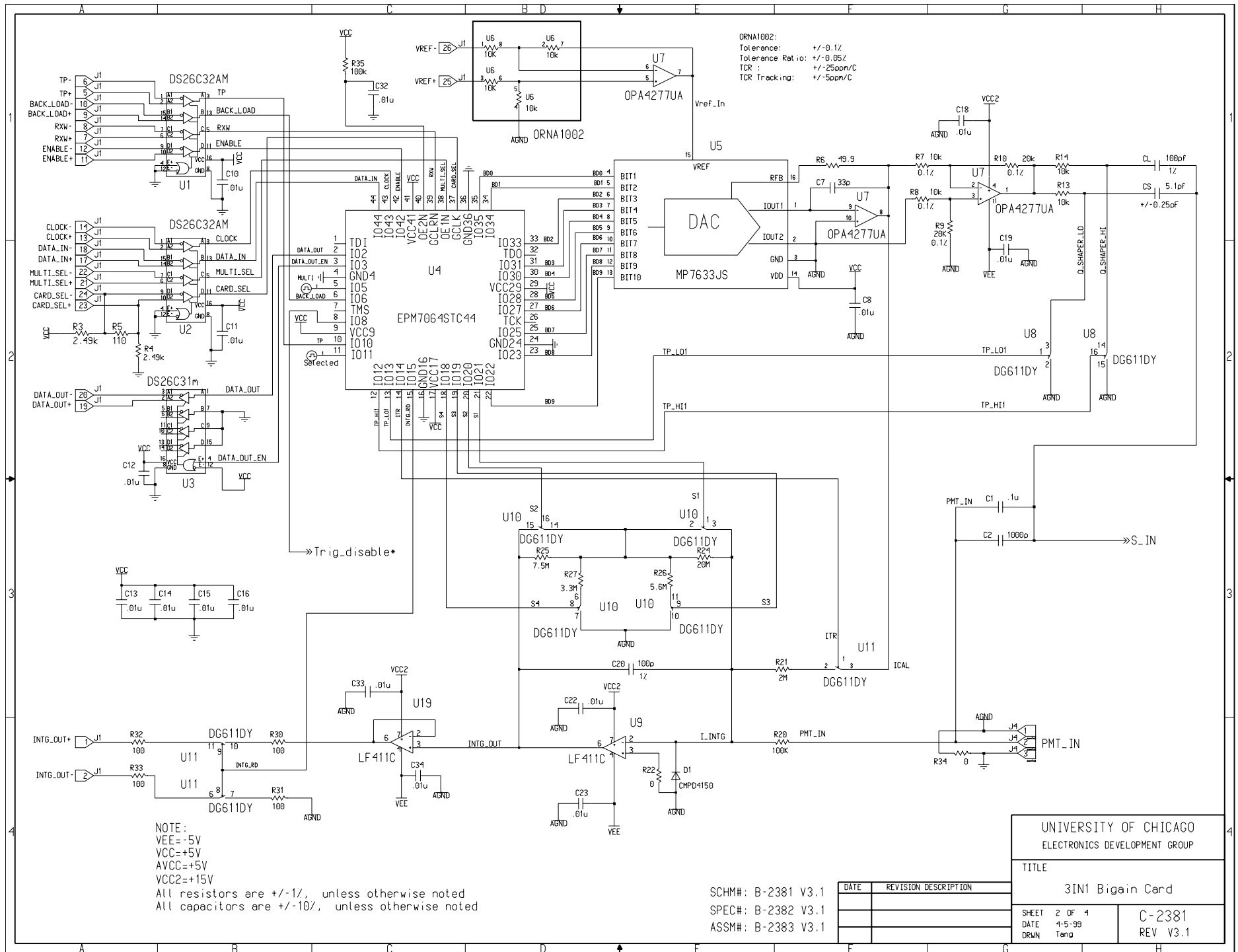
UNIVERSITY OF CHICAGO
ELECTRONICS DEVELOPMENT GROUP

TITLE
3IN1 Bigain Card

SHEET	1 OF 4	B-2381 REV V3.1
DATE	4-5-99	
DRWN	Tang	

DATE	REVISION	DESCRIPTION

SCHM#: B-2381 V3.1
SPEC#: B-2382 V3.1
ASSM#: B-2383 V3.1



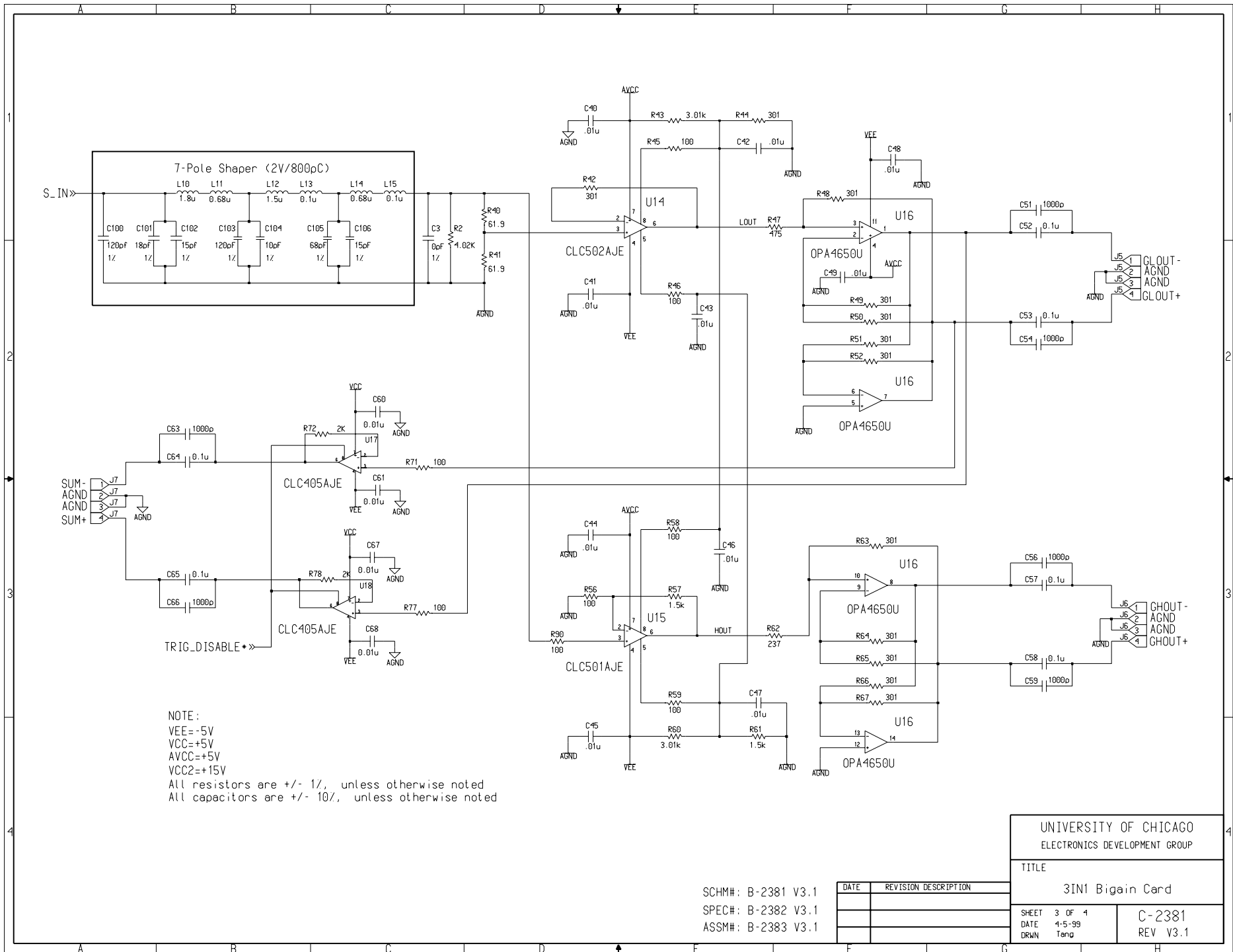
ORNA1002:
 Tolerance: +/-0.1%
 Tolerance Rat io: +/-0.05%
 TCR : +/-25ppm/C
 TCR Tracking: +/-5ppm/C

NOTE :
 VEE=-5V
 VCC=+5V
 AVCC=+5V
 VCC2=+15V
 All resistors are +/-1%, unless otherwise noted
 All capacitors are +/-10%, unless otherwise noted

UNIVERSITY OF CHICAGO ELECTRONICS DEVELOPMENT GROUP	
TITLE 3IN1 Bigain Card	
SHEET 2 OF 4	C-2381
DATE 4-5-99	REV V3.1
DRWN Temo	

SCHM#: B-2381 V3.1
 SPEC#: B-2382 V3.1
 ASSM#: B-2383 V3.1

DATE	REVISION	DESCRIPTION



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 ELECTRONICS DEVELOPMENT GROUP

TITLE

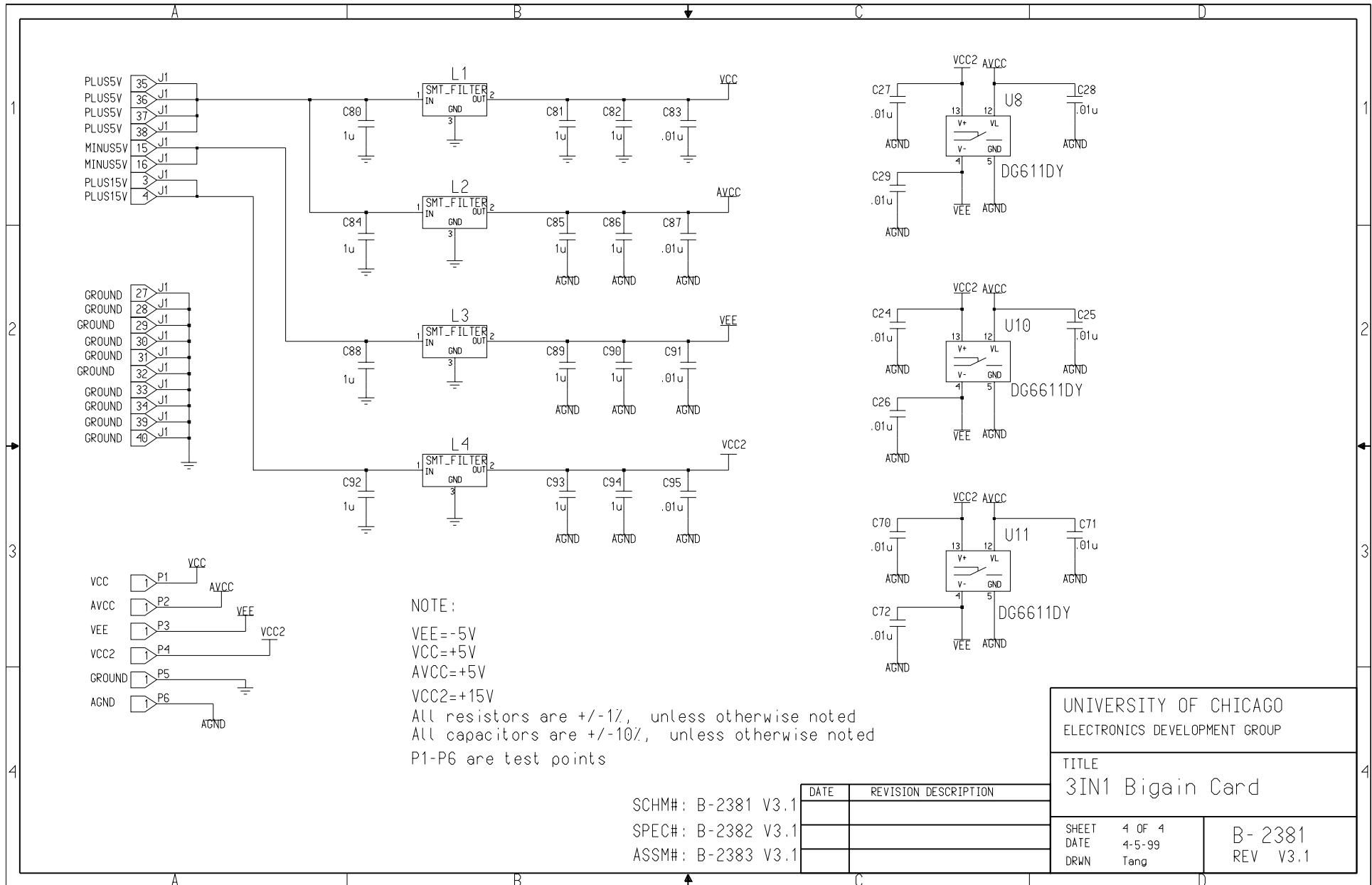
3IN1 Bigain Card

SCHM#: B-2381 V3.1
 SPEC#: B-2382 V3.1
 ASSM#: B-2383 V3.1

DATE	REVISION DESCRIPTION

SHEET 3 OF 4
 DATE 4-5-99
 DRWN Tang

C-2381
 REV V3.1



SCHM#: B-2381 V3.1
 SPEC#: B-2382 V3.1
 ASSM#: B-2383 V3.1

DATE	REVISION	DESCRIPTION

UNIVERSITY OF CHICAGO ELECTRONICS DEVELOPMENT GROUP	
TITLE 3IN1 Bigain Card	
SHEET 4 OF 4	B-2381
DATE 4-5-99	REV V3.1
DRWN Tang	