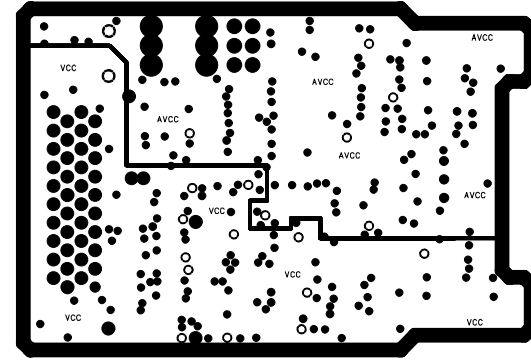
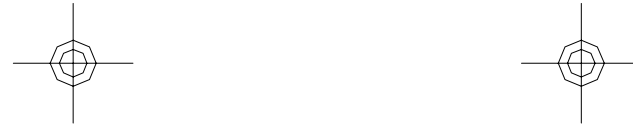
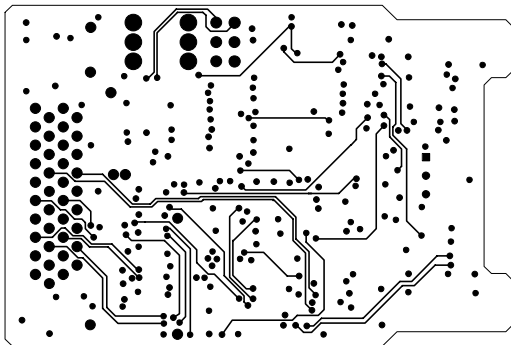


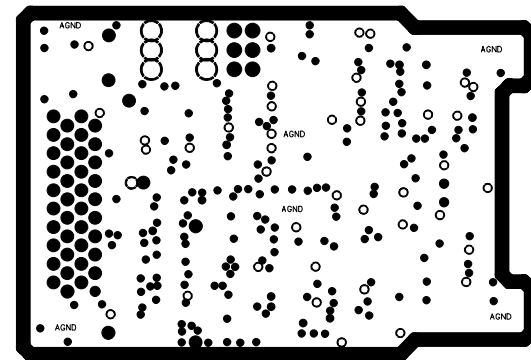
FILE1: LAYER1 (Top layer, SIGNAL\_1)



FILM2: LAYER2 (VCC,AVCC)

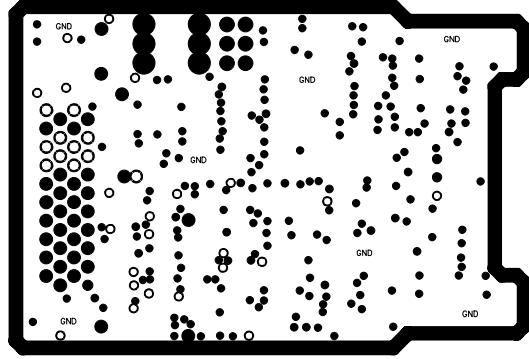


FILM3: LAYER3 (inner signal layer1, SIGNAL\_3)

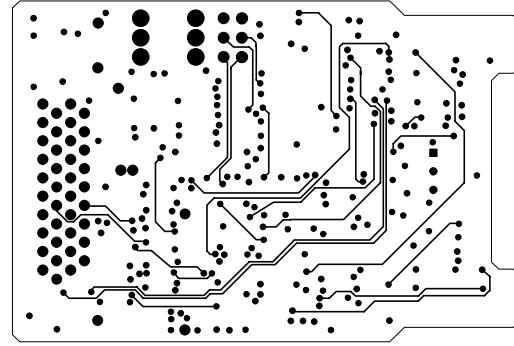


FILM4: LAYER4 (AGND)

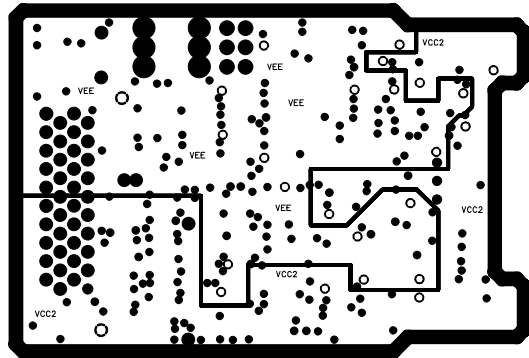
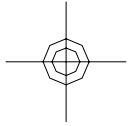
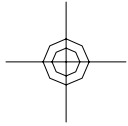




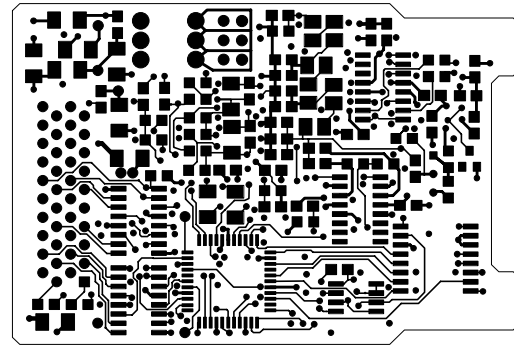
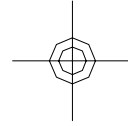
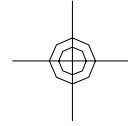
FILM5: LAYERS (GROUND)



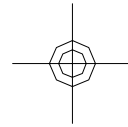
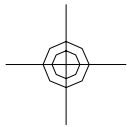
FILM6: LAYER6 (inner signal layer2, SIGNAL\_4)

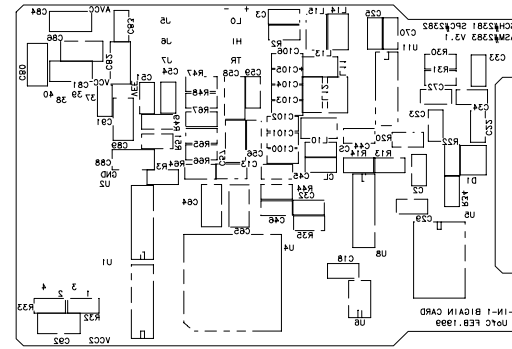
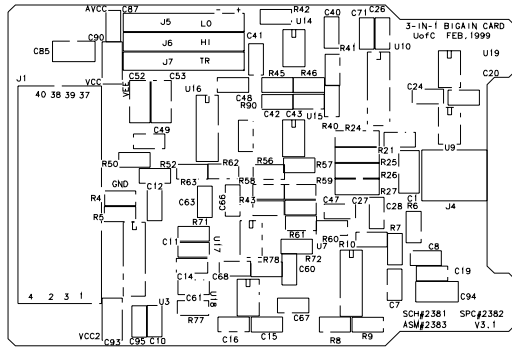


FILM7: LAYER7 (VEE, VCC2)



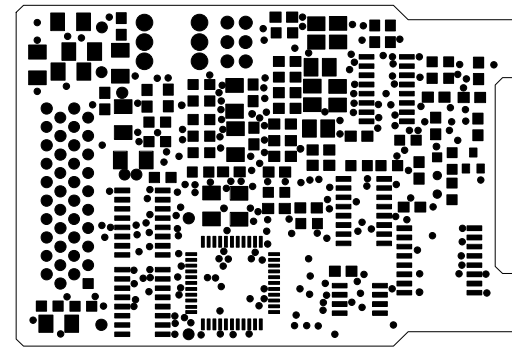
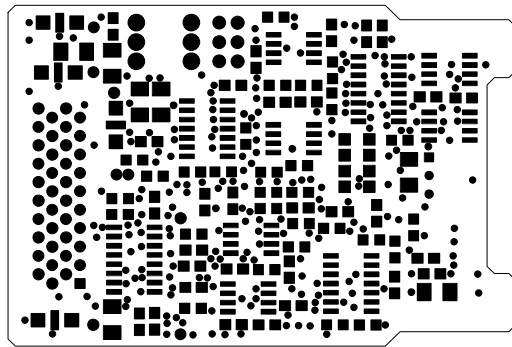
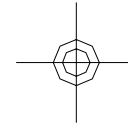
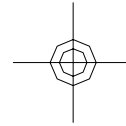
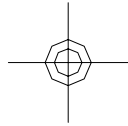
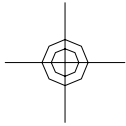
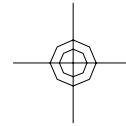
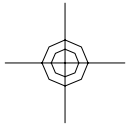
FILM8: LAYER8 (Bottom layer, SIGNAL\_5)





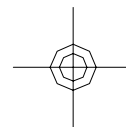
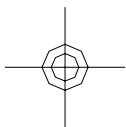
FILM9: Top Silkscreen (SILK\_SCREEN\_1)

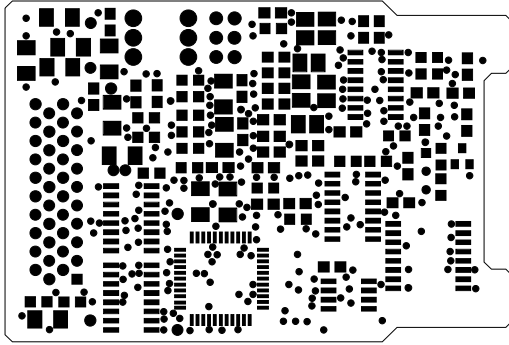
FILM10: Bottom Silkscreen (SILK\_SCREEN\_5)



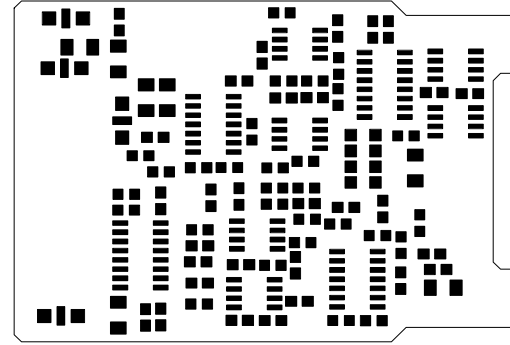
FILM11: Top Solder Mask (SOLDER\_MASK\_1)

FILM12: Bottom Solder Mask (SOLDER\_MASK\_5)

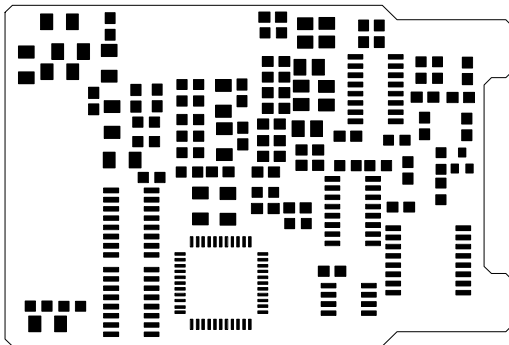
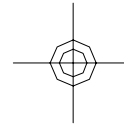




FILM12: Bottom Paste Mask (PASTE\_MASK\_2)



FILM13: Top Paste Mask (PASTE\_MASK\_1)



FILM14: Bottom Paste Mask (PASTE\_MASK\_3)

