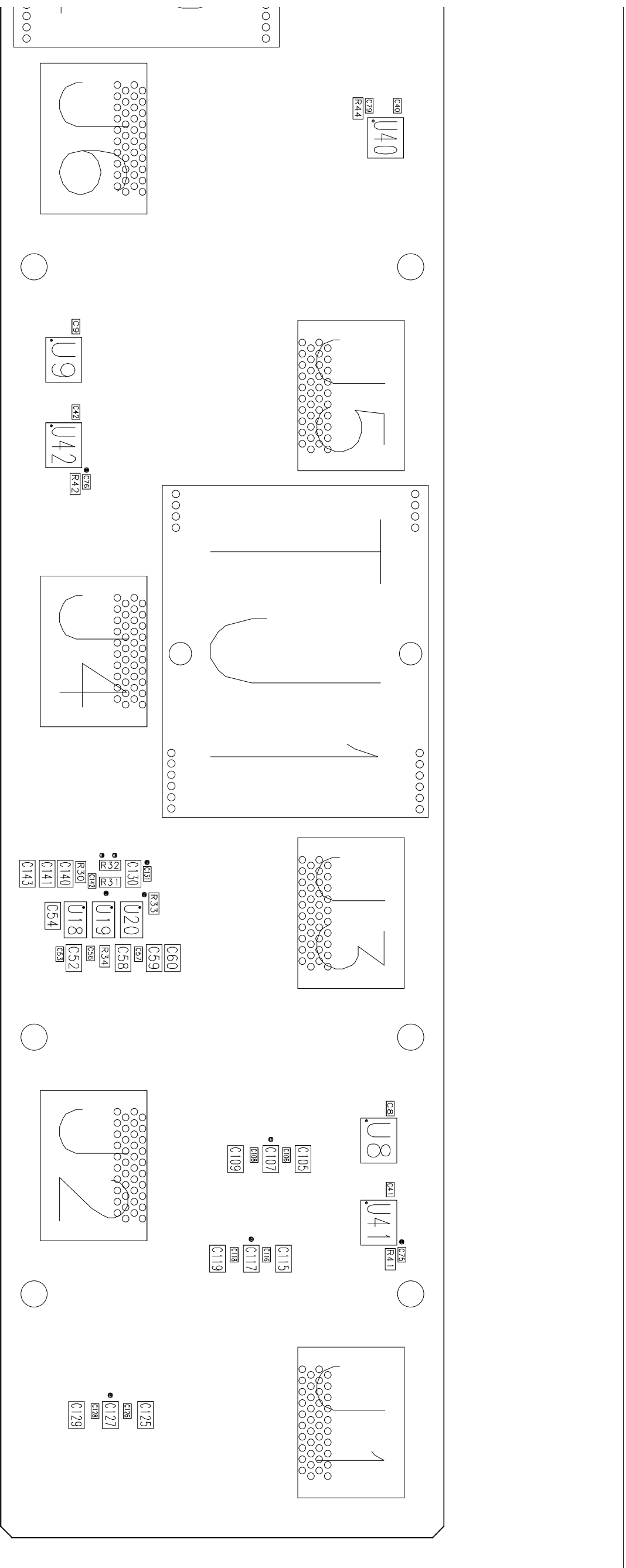


BOARD ORIGIN (0,0)

NOTE :





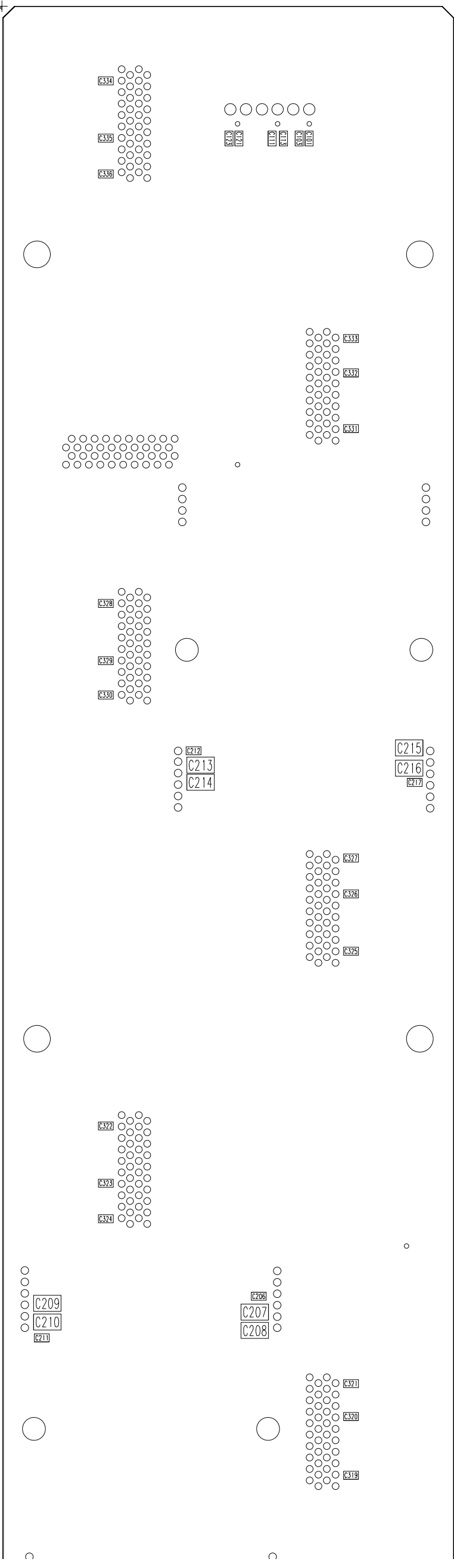
rd-4 Top Side Placement

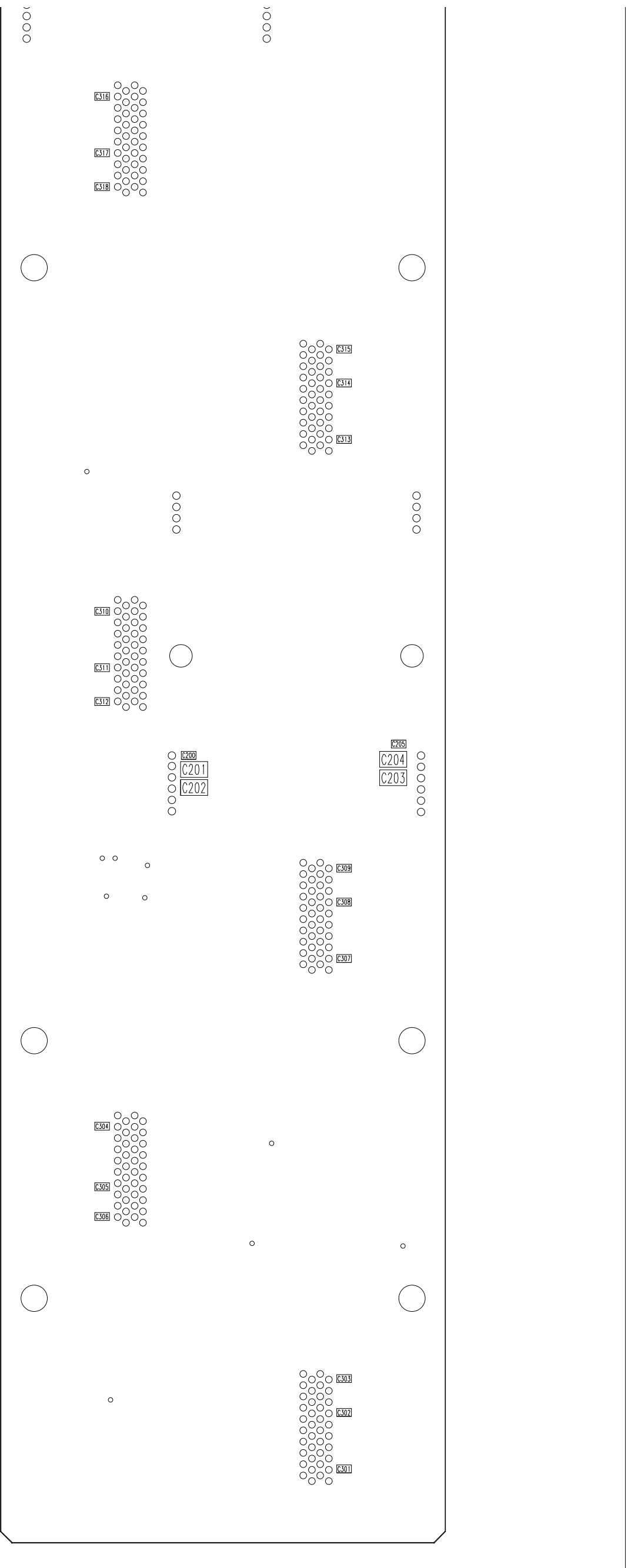
SCHM# 2318 V3.1
 SPEC# 2319 V3.1
 ASSM# 2320 V3.1

<h1>UNIVERSITY OF CHICAGO</h1> <p>ELECTRONICS DEVELOPMENT GROUP</p>	
TITLE	
B2319 V3.1 TOP SIDE ASSEMBLY DRAWING	
SHEET 1 OF 2	B- 2320
DATE 05-08-2000	REV 3.1
DRAWN TANG	

BOARD ORIGIN (0,0)

B2319 V.31 Motherboard-4 f





Bottom Side Placement

SCHM# 2318 V3.1
 SPEC# 2319 V3.1
 ASSM# 2320 V3.1

<h1>UNIVERSITY OF CHICAGO</h1> <p>ELECTRONICS DEVELOPMENT GROUP</p>	
TITLE	
B2319 V3.1 BOTTOM SIDE ASSEMBLY DRAWING	
SHEET 2 OF 2	B- 2320
DATE 05-08-2000	REV 3.1
DRAWN TANG	